

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,768,660 B2  
DATED : July 27, 2004  
INVENTOR(S) : Kong et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 11,

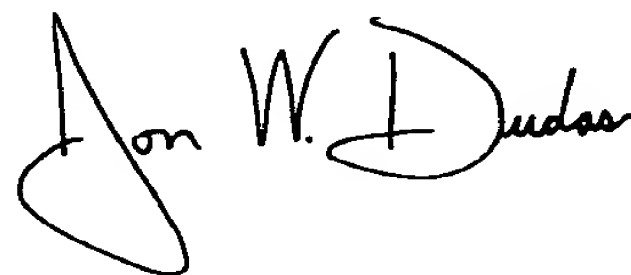
Line 17, should read -- corresponding data pads of each of the integrated circuit --  
Line 51, should read -- which includes a plurality of corresponding address --

Column 12,

Line 7, should read -- **12.** A memory module according to claim **11** wherein the at least --  
Line 32, should read -- **16.** A memory module according to claim **11** wherein the --  
Line 58, should read -- external system clock signal to the at least two integrated --

Signed and Sealed this

Seventh Day of December, 2004

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized, with a large loop for the "J" and a cursive "Dudas".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*